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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/940,155	08/24/2001	David Carroll Challener	RPS9 20010045	6294
53493	7590 04/21/2006		EXAM	INER
LENOVO (US) IP Law			TRAN, TONGOC	
Mail Stop ZHHA/B675/PO Box 12195 3039 Comwallis Road		ART UNIT	PAPER NUMBER	
RTP, NC 27			2134	
			DATE MAILED: 04/21/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/940,155	CHALLENER ET AL.				
Office Action Summary	Examiner	Art Unit				
•	Tongoc Tran	2134				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period was precised to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. sely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 27 De	ecember 2005.					
,— · — — ·	action is non-final.					
3) Since this application is in condition for allowar	•	secution as to the merits is				
closed in accordance with the practice under E						
Disposition of Claims						
4)⊠ Claim(s) <u>1-12</u> is/are pending in the application.						
•	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-12</u> is/are rejected.						
7) Claim(s) is/are objected to.		<i>*</i>				
8) Claim(s) are subject to restriction and/o	r election requirement.	•				
Application Papers						
9) The specification is objected to by the Examiner.						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority document: 2. Certified copies of the priority document: 3. Copies of the certified copies of the priority document: application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicat rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Stage				
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date	4)  Interview Summary Paper No(s)/Mail D 5)  Notice of Informal F 6)  Other:					

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#### **DETAILED ACTION**

1. This office action is in response to Applicant's amendment filed on 12.27/2005.

Claims 1, 5 and 9 have been amended. Claims 1-12 are pending.

## Response to Arguments

2. Applicant's arguments with respect to independent claims have been considered but are most in view of the new ground(s) of rejection.

## **Double Patenting**

3. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory obviousness-type double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent either is shown to be commonly owned with this application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement.

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 1, 5 and 9 are provisionally rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-4 and 8 of

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copending Application No. 10/749,584 and claims 1, 2, 6 and 7 of copending Application No. 10/994,620 in view of Thompson et al. (U.S. Patent No. 6,725,382).

This is a <u>provisional</u> obviousness-type double patenting rejection.

In respect to claims 1, 5 and 9 of the Instant Application, Applicant recites a method, a computer program product and a computer system claims to provide protected storage accessible by BIOS code; storing a symmetrical encryption key in the protected storage; encrypting unaccessible data with the key; storing said encrypted and unencrypted data in EEPROM with write protect algorithms.

Claims 1-4 and 8 of copending Application 10/749,584 claims a modified wake-on-LAN packet using the BIOS over a network (remotely altering data through BIOS); storing the executable code in memory and retrieving the executable code from the memory by an action of BIOS; processing the executable code using the BIOS; verifying the modified wake-on-LAN packet using the BIOS; storing the retrieved executable code to a PARTIES partition of a hard drive associated with the client (storing client associated information in erasable memory). The copending Application fails to recite encrypting the data and storing the encrypted data and the unencrypted data in an EEPROM. However, Thompson teaches a BIOS devices stores BIOS, encrypted password, storing the encryption key and security program in the BIOS device, the BIOS device can be an electrically erasable EEPROM (Fig. 3 col. 3, line 27-col. 4, line 15). Therefore, it would have been obvious to incorporate the teaching of Thompson's encrypting secure data such as password with encryption key stored in the BIOS accessible storage such as EEPROM (write protected algorithm is inherently required in

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order to erase data in the EEPROM) instead of hard drive to ensure secure protection to secret data with removable memory storage.

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Claims 1 and 2 of the copending Application 10/994,620 recites securely logon to a program using a password; encoding the first password and storing the encoded first password in a Trusted Platform Module; decrypting the encrypted program password; logging to the program with the program password; storing the encrypted program password in a non-volatile memory. In the copending Application, the claim recites storing the encoded password in the Trusted Platform Module (Fig. 1A, Trusted Platform Module interface (encompasses encryption module and decryption module) interfaces with logon module). The copending Application does not recite the protected storage store the encryption key and accessible only by the BIOS and the encrypted password and the unencrypted data stored in EEPROM. However, Thompson discloses encryption is stored in the BIOS device and is accessible by the BIOS and the BIOS device can be an electrically erasable EEPROM (Fig. 3, col. 3, line 27-col. 4, line 15). Therefore, it would have been obvious to incorporate the teaching of Thompson's storing encryption key in the BIOS device with erasable EEPROM instead of hard drive to ensure secure protection to secret data with removable memory storage.

Claims 2-4, 6-8 and 10-12 are also rejected because by their dependency, they contain the language of the independent claims.

Claim Rejections - 35 USC § 102

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4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-3, 5-7 and 9-11 are rejected under 35 U.S.C. 102(e) as being anticipated by Thompson et al. (U.S. Patent No. 6,725,382, hereinafter Thompson).

In respect to claim 1, Thompson discloses a method for securing alterable data in a remotely managed system comprising the steps of:

Providing protected storage accessible only by Basic Input Output System (BIOS) code (see Fig. 3, col. 3, lines 10-26 and lines 47-57);

Storing a symmetrical encryption key in said protected storage (see col. 3, lines 47-57);

Encrypting normally unaccessible (NA) data with said symmetrical encryption key (see Fig. 3, item 306 col. 3, lines 47-57); and

Storing said encrypted NA data and accessible non-encrypted (ANE) data in an unprotected electronically erasable programmable read only memory (EEPROM) with existing write protected algorithms (see col. 3, line 27-col. 4, line 5, EEPROM, write protected algorithms is inherently required in order for the data stored in the EEPROM to be erased).

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In respect to claim 2, Thompson discloses the method of claim 1 further comprising the steps of:

Altering said ANE data by issuing an existing write request to said BIOS from said write protected algorithms for said EEPROMS; and updating said ANE data in said EEPROM (see col. 3, lines 27-46 and col. 7, lines 26-44).

In respect to claim 3, Thompson discloses the method of claim 1, further comprising the steps of:

Accessing said NA data via a change request issued to said BIOS over a secure communication link; Validating said changed request (see Fig. 1, item 130, col. 5, line 45-col. 6, line 67);

Retrieving said symmetrical encryption key by said BIOS in response to said validating change request; Using said symmetrical encryption key to decrypt and alter said NA data; encrypting said altered NA data using said symmetrical encryption key; and storing said altered encrypted NA data in said EEPROM (see col. 3, lines 27-46 and col. 7, lines 25-44).

In respect to claims 5-7 and 9-11, the claimed limitations are computer program product and computer system claims that are substantially similar to method claims 1-3. Therefore, claims 5-7 and 9-11 are rejected based on the similar rationale.

Claim Rejections - 35 USC § 103

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5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 4, 8 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Thompson (U.S. Patent No. 6,725,382) in view of Mirov et al. (U.S. Patent No. 6,138,236, hereinafter Mirov).

In respect to claim 4, Thompson discloses the method of claim 1. Thompson does not disclose but Mirov discloses the steps of:

encryption key; Storing said encypted hash with said ANE data; Computing a hash of configuration data in said ANE data on boot-up request; Decrypting said stored encrypted hash of said configuration data; Comparing said decrypted hash of said stored configuration data to said computed hash of said configuration data from said ANE data; Booting normally in response to a compare of said decrypted hash and said computed hash and Issuing tamper notification and initiating recovery processes on a non-compare of said decrypted hash and said computed hash (see Mirov, col. 2, lines 21-32 and col. 3, line 55-col. 5, line 50). It would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the teaching of Thompson's password based security program with Mirov's teaching of implementing hashing system to authenticate plurality of micro-code to authorize execution of micro-code to ensure the integrity of the stored data.

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In respect to claims 8 and 12, the claimed limitations are computer program product and computer system claims that are substantially similar to method claim 4. Therefore, claims 8 and 12 are rejected based on the similar rationale.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tongoc Tran whose telephone number is (571) 272-3843. The examiner can normally be reached on 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques Louis-Jacques can be reached on (571) 272-3962. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner: Tongoc Tran

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April 12, 2006